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TESTING A SOFTWARE
COMPONENT USING AN
ABSTRACTION MATRIX

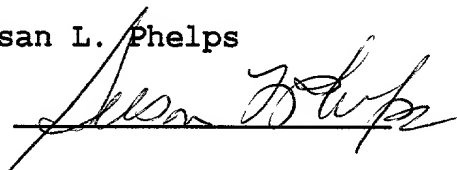
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**METHOD AND APPARATUS FOR TESTING A SOFTWARE
COMPONENT USING AN ABSTRACTION MATRIX**

Technical Field

[0001] The present invention relates generally to methods and apparatus for software testing, and more specifically, to a functional testing technique employing an abstraction matrix for facilitating testing of complex software components such as an operating system component.

Background of the Invention

[0002] Software testing is an ongoing task in computer software program development and maintenance which requires a large portion of development time, computer and human resources, and effort. Software development may include the development of an entirely new application or program, or the addition of a new feature to an existing application. Software maintenance activities generally include the correction of reported problems.

[0003] Testing is performed with the goal of verifying the correct functioning of new software and modifications to existing software. Generally, software testing accompanies even minor code modifications or enhancements to ensure correctness. Verifying the correctness of software may involve numerous tasks ranging from ensuring correct coding syntax through successful compilation, to checking the

execution results by examining the output of a software program.

[0004] In order to test the execution of software, a machine-executable program comprising binary instructions and data in machine-readable form must be produced and executed. The software may be written in some high-level or low-level programming language. This software may be processed by a compiler, language processor, or translator to produce an object file containing binary or machine-readable code. Usually, there are multiple software modules or source files (herein generically referred to as software components) which comprise the software to be tested. Each of these software modules may be separately compiled, and a separate object file may be produced for each of these source files. These object files may be combined into a single machine-executable program using an operating system tool such as a linker which links together the multiple object files containing binary code and data to produce a single combined machine-executable program containing binary code and data. This machine-readable program may be run or executed on a computer system, and the results from the execution may be examined as a means to ensure correct functioning of the software.

[0005] Software programs vary in complexity and size. Both the small and simple programs as well as the large and more complex programs have a need for efficient software testing. Generally, as complexity and size increase, the

amount of testing and the need for efficient testing increases as well.

[0006] For example, if the goal is to test a complex software component (such as a "cluster operating system") as a black box, two problems are immediately faced. First, there will be an enormous number of test cases based on the number of potential inputs (i.e., the software is multi input and multi exit), without necessarily validating the validity of the individual tests. In addition, a large amount of time will be required in order to execute all of the tests needed to verify the complex software component.

[0007] Thus, the present invention comprises in a general aspect, enhanced methods and apparatus for testing software, and more particularly, to enhanced techniques for efficiently testing a complex software component in order to minimize the number of test cases required to validate the software component and reduce overall time required to execute the test cases.

Summary of the Invention

[0008] The shortcomings of the prior art are overcome and additional advantages are provided through the provision of a method of testing a software component, including: creating an abstraction matrix that describes the software component, the abstraction matrix including state and event information; parsing the abstraction matrix to generate test cases and mapped expected results therefore; separating the

test cases based on layers of the software component, and associating data structures with the separated test cases of the layers, the data structures allowing the test cases of the various layers to be uncorrelated; employing a software component in executable form to generate for each layer of the software component test case execution threads from the test cases and map expected results for that layer; and executing in parallel at least some of the test case execution threads for at least one layer of the software component, thereby testing the software component.

[0009] In another aspect, a method of generating test cases for use in testing a software component is provided. This method includes: ascertaining a functional specification of the software component; creating an abstraction matrix that describes the software component using the functional specification, the abstraction matrix comprising state and event information; parsing the abstraction matrix to generate test cases and mapped expected results therefore; and separating the test cases based on layers of the software component, and associating data structures with the separated test cases of the layers, the data structures allowing the test cases of the various layers to be uncorrelated.

[0010] System and computer program products corresponding to the above-summarized methods are also described and claimed herein.

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[0011] To restate, provided herein is a functional testing technique employing an abstraction matrix for facilitating testing of complex software components. The technique is nonintrusive to the software code being verified, and allows generation of test cases so that there is full code coverage in the least amount of test cases. Additionally, the technique presented herein can factor test cases to only reflect specification coverage, and provides the ability to prioritize test cases so that the most important cases can be executed first. When used with a staged function delivery, the technique presented can factor out all "doable" test cases. Thus, making the most use of the available testing resources. In addition, the technique of the present invention allows mapping of code changes to test cases. Thus, the full cost of a coding change can be derived by understanding the amount of testing needed to support the change.

[0012] Additional features and advantages are realized through the techniques of the present invention. Other embodiments and aspects of the invention are described in detail herein and are considered a part of the claimed invention.

Brief Description of the Drawings

[0013] The subject matter which is regarded as the invention is particularly pointed out and distinctly claimed in the claims at the conclusion of the specification. The foregoing and other objects, features, and advantages of the

invention are apparent from the following detailed description taken in conjunction with the accompanying drawings in which:

- [0014] FIG. 1 depicts one example of a conventional method for testing a software component;
- [0015] FIG. 2 is a general diagram of a multilayer software component to be tested;
- [0016] FIG. 3 is a flowchart of one embodiment of a software testing process in accordance with the principles of the present invention;
- [0017] FIG. 4 is a block diagram of one embodiment of an automated testing apparatus in accordance with the principles of the present invention;
- [0018] FIG. 5 is a block diagram of one embodiment of an abstraction parsing process in accordance with the principles of the present invention;
- [0019] FIG. 6 is a block diagram of one embodiment of a functional verification test apparatus in accordance with the principles of the present invention;
- [0020] FIG. 7 is an example of a state change from a current state to a next state to be recorded in an

abstraction file constructed in accordance with the principles of the present invention;

[0021] FIG. 8 is a simplified example of an abstraction file in accordance with the principles of the present invention;

[0022] FIGS. 9A-9D are a flowchart of one process embodiment of test case and mapped expected results creation employing an abstraction matrix in accordance with the principles of the present invention;

[0023] FIG. 10 is an example pseudocode implementing the abstraction engine and mapped expected results functions of FIG. 4;

[0024] FIG. 11 is a flowchart of one embodiment of functional verification test processing in accordance with the principles of the present invention;

[0025] FIGS. 12A & 12B comprise one example of pseudocode implementing the functional verification test process of FIG. 11; and

[0026] FIG. 13 is a block diagram of a computer system which can be employed to implement the automated features of the present invention.

Best Mode for Carrying Out the Invention

[0027] Generally stated, presented herein is an enhanced facility for testing a complex software component, such as an operating system component. An objective of this testing facility is to identify the minimal number of states required to model a software component, with an intent to understand the state changes and subsequently identify the invariant intra-layer and inter-layer state properties. The testing procedure is to evaluate the invariance and verify that the software component is either in a consistent state or an inconsistent state (i.e., contains a defect). The test cases that will be used are event scenarios, which reflect state changes.

[0028] One method to deriving test cases is to use a state diagram. However, for a large, complex software component (such as a "Cluster Operating System"), where n independent Boolean attributes affect control, the diagram will lack expressive power, and is indeed impractical due to the enormous amount of space required to represent it (i.e., 2^n , where n represents the number of states). Another way to look at this problem is the fact that for a multi-entry, multi-exit model, the number of tests needed to provide coverage will be exponential. What is needed is a method to "uncorrelate" the inputs, thus reducing the number of tests needed for coverage.

[0029] The solution proposed herein is to partition the software component into states which are traversed by

events, thus mathematically requiring only $2n$ states. This is accomplished by deriving a conceptual layering scheme which allows the construction of independent tests per layer. The independent layers take into account the relationships (i.e., data structure) that exists between each layer, thus allowing independent tests per layer to be constructed, and reducing the number of tests needed to verify the software component. The apparatus for implementing this technique includes an abstraction engine which is employed to automatically extract the information used to generate the inputs, and mapped expected results for each layer.

[0030] A driver is then employed to execute the test cases of the different layers concurrently, thus reducing the overall time it takes to test the software component. By reducing the time per testing of the software component, full test/regression testing of a software component is possible in a relatively short amount of time. Further, the techniques presented herein can be used with any level of software development (i.e., unit test, function test, integration test). The testing facility of the present invention can also be employed commercially to help evaluate the quality of a given software component.

[0031] One embodiment of a conventional software testing approach is depicted in FIG. 1. This testing process, generally denoted 100, essentially comprises a manual process for the creation of test cases. The process begins with creation of a software specification 110, which is used

in software development 120 to create a software component 130 to be tested. Commensurate with software development, a test plan 140 is manually developed from the software specification 110. Test cases are then created from the developed test plan 150. Test case execution 160 is a serial process wherein the software component, manually created test cases and user defined attributes are received as inputs and test cases are tested and completed in sequence, typically beginning with higher layers of the software component. Thus, upon completion of test case 1 test case 2 is executed, upon completion of test case 2, test case 3 is executed, etc. until test case n is executed and completed.

[0032] FIG. 2 depicts a simplified version of a software component 200 having multiple layers 220. Software component 200 comprises a multi-input 210, multi-exit 230 software component to be tested. As a single example of an external event input, one input signal may comprise a cluster manager input for n nodes as shown, and if testing in the conventional manner, an output may comprise a log file indication of the cluster health. Using conventional software testing, the test cases are on the periphery of the boundary of the software component and testing of the component traverses from a top layer all the way down to the bottom layer.

[0033] One generalized embodiment of software testing in accordance with the principles of the present invention is depicted in FIG. 3. This testing process, generally denoted

300, again proceeds from a software specification 310. Software development 320 employs the software specification to create the software component 330. However, in this case an abstraction 340 of the software specification is created which is then employed to create individual test cases for the different layers of the software component. The abstraction, which is described further below, comprises in one embodiment a matrix that describes the software component. The matrix includes state and event information breaking the software specification down into a mathematical expression. For each layer of the software component, the abstraction matrix can comprise an abstraction file which contains current state and next state information with events which are necessary to go from a current state to a next state. After the abstract has been created and approved, the abstraction matrix is employed in an abstraction engine/apparatus 350 to generate test cases for output and execution in parallel. The abstraction engine/apparatus 350 preferably comprises an automated process for generating the syntax for the test cases and subsequently executing the test cases. This engine/apparatus reads the list of abstractions, that describe the various parts of the software component, parses the list, creates test cases needed to validate the layers of the software component by breaking the test cases into sections associated with each layer of the software component, identifies the mapped expected results for the various test cases and allows simultaneous testing of the software component layers.

[0034] The following discussion is an example of how to construct an ABSTRACTION file. As shown in FIG. 3, an ABSTRACTION 320 is created, based on the information contained in the Software Specification. An example of what that specification might look like is as follows:

""In order to create a table inside the directory you must do the following,

```
mksrtbl</Dir/Name_of_table><col=col_name_1>  
<col=col_name-2>...<col=col_name_n>
```

Where /Dir refers to the directory name, Name_of_table refers to the name of the table, and col_name refers to the name of the column. You may have up to 30 columns in the table, all of which can have names of up to 256 characters, for any column name""

[0035] The example above might represent a small section of a typical software specification. Based on the specification, the ABSTRACT would be the following:

CS (Current State) is a Directory i.e. /DIRECTORY, that currently exists.

The NS (Next State) would be the table, with n numbers of columns i.e.

/DIRECTORY_/Name_of_table_file_and_n_number_of_columns

[0036] The event scenario that allows you to get to the next state (NS) is

```
<mksrtbl/DIRECTORY/Name_of_table><col=col_name_1>...  
<col=col_name_n>
```

A user selected name of a Directory, column name, and the number of columns would all be considered attributes of the event scenario.

[0037] Thus the ABSTRACTION that would be constructed is the following:

```
<CS:DIRECTORY || NS:DIRECTORY/Name_of_table_file_and_  
n_number_of_columns
```

This would represent a typical line in the ABSTRACTION file.

[0038] CS: is a token which identifies that this is the current state. The next token is the name of a file and the name of the current state. Thus the existence of "DIRECTORY" is the current state, and the name of the file that will contain the current state information. The current state information will contain the event scenario.

[0039] The character <||> separates the CS token from the NS token. NS is a token which identifies the next set of characters, which is the name of a file, which contains the next state information.

[0040] The file named <DIRECTORY> will contain the following set of information

```
1 STATE DIRECTORY
2 mount_directory/DIRECTORY
3 create_directory/DIRECTORY
```

[0041] Below is an explanation of each line.

Line 1 is the exact syntax used for this state and or the state name, and is an indicator that the next line(s) would be a set of events that are necessary to be at this particular state. This is analogous to a simulated set of events, which allows each state to be tested independently.

Lines 2 and 3 are the conditions that must exist in order to be at that state. This is analogous to a simulated set of events, which allows each state to be tested independently.

[0042] The DIRECTORY, Name of table, col name 1 and number of columns, are all variables, in which the tester can plug in any type that is deemed needed i.e., attributes.

[0043] Finally, the NS name <DIRECTORY_/Name_of_table_file_and_n_number_of_columns>, would be the name of a file which contains the mapped expected result. Based on the original description in the specification, a generic representation would be made in the file to represent a full director path, with a file (table) which would have N numbers of columns.

Example:

```
1 STATE mksrtbl/DIRECTORY/Name_of_table col=col_name  
2 col_name_data  
3 number_of_columns
```

Line 1 is the exact syntax used for this state and or the state name.

Lines 2 and 3 are the conditions that must exist in order to be at that state. In this case col_name_data is a pointer to a data set which describes the conditions (i.e. 256 characters) of this variable. The line 3, number_of_columns is a variable which would contain 1 to 30, thus an attribute which can be filled in by the tester.

[0044] Continuing with the drawings, FIG. 4 depicts one embodiment of an abstraction engine/apparatus for generating test cases in accordance with the principles of the present invention. In this example, an abstraction engine, described further below in connection with FIGS. 9A-10, receives as input an abstraction matrix which comprises a list for files of abstractions which describe the various layers (i.e., sub-components) of the software component. In addition, a list of event entries is input to the abstraction engine 410. The mapped expected results 420 are identified from the abstraction matrix. For example, the mapped expected results comprise specific states of the software component and test cases are generated associated with the particular mapped expected results. An automated

technique for identifying mapped expected results is described below in connection with FIGS. 11 & 12.

[0045] Briefly explained, FIG. 5 depicts an abstraction process 500 which takes the abstraction matrix or list 510 and parses the individual abstraction lines 520 to create event scenario data 530 and mapped expected result data 540. Since the abstraction matrix preferably is created with abstraction files for each layer of the software component, the event scenario and the mapped expected result information is layer specific as well.

[0046] Continuing with FIG. 4, the test cases associated with the individual layers 430 are separated and data structures for the different layers 435 are associated with the separated test cases. The data structure is information that allows the layers to be uncorrelated. These structures simulate what the inputs would normally be if outside the bounds of a component layer. That is, the data structure is constructed to mimic what is happening within a layer or between layers. For example, the data structure for layer 1 may mimic the inputs that are typically received for layer 1, while a data structure for layer 2 would mimic the information that is typically passed from layer 1 to layer 2 during operation of the software component.

[0047] A functional verification test apparatus 440 next takes the separated test cases and associated data structures for the layers and employs them with the software component executable 450 to generate individual test case

execution threads for each layer of the software component. These individual test case execution threads can then be simultaneously executed in parallel, thereby achieving a significantly compressed testing interval compared with the conventional sequential testing approach described above in connection with FIG. 1. Further, those skilled in the art will note that in accordance with the present invention software testing employs the software component executable only; that is, there is no need for the source code, and testing in accordance with the present invention is not intrusive.

[0048] FIG. 6 depicts a more detailed embodiment of a functional verification test apparatus in accordance with the principles of the present invention. This apparatus receives as input the mapped expected results for the different layers, e.g., layer (i) 600. The event classes or event states 610 comprise the mapped expected results for the layer. These states are input 622 to a queue 630 within the functional verification test apparatus 620. Also input to queue 630 are the event scenarios or test cases for layer (i) 621 and the software component executable 623 to be tested. The data is extracted from queue 630 to create execution threads 640, which may be output in parallel for compressed testing of the software component. Again, as shown in FIG. 4, the test execution threads can be generated in parallel for all layers of the software component.

[0049] FIG. 7 depicts a simple state diagram wherein the software component transitions from a current state (CS) 700

to a next state (NS) 710 upon occurrence of an event, which in this case is defined as Hb_JOIN + HB_NEW_GROUP.

[0050] FIG. 8 depicts one example of an abstraction file for a layer (e.g., layer 2) of a software component to be tested in accordance with the principles of the present invention. This abstraction file 800 comprises part of an abstraction matrix created in accordance with the present invention. As shown, the abstraction file 800 includes multiple listings 810, one of which is defined by way of example. This listing comprises current state information (CS), i.e., node adapter up, and next state information (NS), AMG_STATE = stable, which means that the adapter monitor group state is stable. Testing in accordance with the present invention determines whether there is going to be a problem in executing the software component executable to go from the current state to the next state. The current state and next state information is in the abstraction matrix which is created directly from the functional specification. Again, the functional specification is a document which describes the software component, and in fact, is used to generate or develop the software component. An abstract or abstraction matrix as employed herein describes the software component in mathematical abstract form.

[0051] FIGS. 9A-10 describe one process whereby each line or abstract list for a particular layer is read in order to ascertain current state and next state information. The current state name is a pointer to a file which contains

specific events that will cause the software component to go from the current state to the next state. The abstraction engine goes into the file and obtains a list of those events, which are written in a generic fashion. The events are then completed with the attributes list provided.

[0052] FIGS. 9A-9D are a flowchart of one embodiment of abstraction engine and mapped expected results engine processing in accordance with the principles of the present invention. The processing 900 of this flowchart extracts and separates the state and event information into the different layers of the software component. Thus, input to the abstraction engine is the abstraction matrix or list 905 which contains state and event abstract information for the different layers of the software component. Engine processing starts 910 by obtaining the abstract list for the software component 915. Processing determines whether the list is empty 920, and if "yes", then performs functional verification testing 925, thereby generating test case execution threads as described above in connection with FIGS. 4 & 6. After generating the threads, processing is complete 930.

[0053] Assuming that the abstract list contains abstract files yet to be processed, then processing continues from inquiry 920 with the processing of FIG. 9B. In FIG. 9B, processing opens a next abstract file 935 and determines whether all lines of the file have been processed 940. If so, then processing of that particular file has been completed 945. Otherwise, the next line of the abstract

file is obtained 950. As noted above, this line contains current state and next state information.

[0054] A current state file is obtained from the current state token name contained in the abstract line 955 and a template script is generated therefrom 960. The event scenario or test case is copied into the script 965 (FIG. 9C) and the attributes associated with the current state are obtained 970. Processing determines whether the list of attributes is empty 975, and if not plugs an attribute into the event scenario and writes the event scenario into the template script 980. The process continues until all attributes have been plugged into the event scenario.

[0055] Once the attributes list is empty, processing acquires the next state information for the particular line 985 and generates a mapped expected results script 990. The next state information is then copied into the mapped expected results script 995 and both the template script and the mapped expected results script are stored in a file named after the particular software component layer 997. Processing then returns to inquiry 940 to determine whether an end of the abstract file for that layer has been reached, and if so, processing for that layer is complete 945. The above-summarized process repeats for each layer of the software component.

[0056] FIG. 10 depicts one example of pseudocode for an abstraction engine and point of verification (i.e., mapped

expected results) generation for each layer of the software component.

[0057] FIG. 11 depicts a flowchart of one embodiment of functional verification test processing to be implemented for each layer of the software component. Processing 1100 begins 1110 by initiating an execution thread 1120 and a mutex 1130. A file that contains the scripts stored during the processing of FIGS. 9A-9D is opened and test cases are retrieved beginning at the top of the file 1140. Inquiry is made whether the end of this file has been reached 1150. If not, then the next script is obtained 1170 and processing creates a thread, performs mutex lock, executes the script, and then mutex unlocks the executable 1180. Once the file has been fully processed, processing is complete 1160. FIGS. 12A & 12B comprise pseudocode of one example of functional verification test processing in accordance with the principles of the present invention.

[0058] To restate, presented herein is a technique for describing each layer of a large, complex software component by using "abstractions". These abstractions serve as input to an automated process for generating test execution threads for parallel execution. The technique includes establishing a syntax for the abstraction to allow for easy parsing. For example, < state name >: Boolean expression for event scenarios. That is:

State_name1: (A+B), C

Where state_name1 is just that, a state which indicates a particular property of the software layer, and A, B and C are event classes. An event class is an action which could have many types of events (attributes) that can be used. For example, a Resource could be an event class, and events (like "node up") could be considered one of many attributes that could be associated with the event class "Resource". Further, "+" means "or" and "," means "and".

[0059] The technique next involves creating the abstraction file for each layer. Taken together, the abstraction files comprise the abstraction matrix. The abstraction file is a function which describes a given state by means of a Boolean expression of event scenarios which express what the conditions (events) need to be to acquire a given state. A parser is then provided for parsing each layer abstraction. That is, for each line of an abstraction file, the parser generates a mapped expected result based on the associated state name and creates a data structure which will house the event class scenario.

[0060] To summarize, those skilled in the art will note that pursuant to the present invention, the total number of tests required to verify a software component is reduced to a minimum number of tests by uncorrelating the layers, that is, by eliminating the dependency that some inputs have on other inputs. This can result in a 2^n versus $2n$ reduction in the overall number of test cases needed to verify a software component. The resultant test cases are event

scenarios which are focused on specific mapped expected results. Worded another way, the technique comprises:

1. Partitioning the high level state diagram to n independent state machines (i.e., layers);
2. For each layer, expand the state machine into subordinate state diagrams.

These items are accomplished by using the abstraction for each layer as summarized above. Each abstraction is identified as a description of an object, where each object is a "layer" of the software component. Each layer thus has its own independent model. After parsing the abstraction matrix, the event classes are generated, and for each event class, specific events (attributes) are inserted as provided on an attributes list. This list contains attributes that based on the abstraction are specific events that are associated with each event class, which allow for independent tests via simulation of inputs by way of inserting events into the derived structure.

[0061] Using the abstraction, which expresses the event class associated with each state, the mapped expected results are generated. Each abstraction property identifies a set of Boolean expressions of event class(es) which describe a state. This state is considered a mapped expected result. The Boolean expression is equivalent of an event scenario.

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[0062] In accordance with the present invention, the overall test execution time is reduced by executing generated test case execution threads concurrently for each layer. Testing is further optimized by executing the tests within a given layer concurrently as well. The result is a dramatic reduction in the overall test execution time, which means that it is cost effective to use all the tests for regression test purposes. For example, for each test set associated with a given layer, the invention comprises invoking the tests on separate machines concurrently. For each machine, threads are created per scenario (i.e., test case), thus causing all the tests in a given machine to be executed concurrently. Each test execution has a mapped expected result which can be used for comparison.

[0063] In one embodiment, the software component testing facility of the present invention is incorporated and used in a computing system, such as the one depicted in FIG. 13. Computing system 1300 includes, for instance, one or more central processing units 1320, a main storage 1340 and a storage system 1360, each of which is described below.

[0064] As is known, central processing unit (CPU) 1320 is the controlling center of computing system 1300 and provides the sequencing and processing facilities for instruction execution, interruption action, timing functions, initial program loading and other machine related functions. The central processing unit executes at least one operating system, which as known, is used to control the operation of the computer by controlling the execution of other programs,

controlling communication with peripheral devices and controlling use of the computer resources. The storage and retrieval facility of the present invention is, in one embodiment, controlled by the operating system, similar to that of other computer programs.

[0065] Central processing unit 1320 is coupled to main storage 1340, which is directly addressable and provides for high speed processing of data by the central processing unit. Main storage may be either physically integrated with the CPU or constructed in stand alone units.

[0066] Main storage 1340 is also coupled to storage system 1360, which includes one or more of a variety of input/output devices, such as, for instance, keyboards, communication controllers, teleprocessing devices, printers, magnetic storage media (e.g., tape, disks), direct access storage devices, and sensor based equipment. Data is transferred from main storage 1340 to storage system 1360, and from the storage system back to main storage.

[0067] One example of computing system 1300 incorporating and using the software testing facility of the present invention is an RS/6000 computer system offered by International Business Machines Corporation. This is only one example, however. The present invention can be used within other computing environments or with other computer systems without departing from the spirit of the present invention.

[0068] The present invention can be included in an article of manufacture (e.g., one or more computer program products) having, for instance, computer usable media. The media has embodied therein, for instance, computer readable program code means for providing and facilitating the capabilities of the present invention. The article of manufacture can be included as a part of a computer system or sold separately.

[0069] Additionally, at least one program storage device readable by a machine, tangibly embodying at least one program of instructions executable by the machine to perform the capabilities of the present invention can be provided.

[0070] The flow diagrams depicted herein are just examples. There may be many variations to these diagrams or the steps (or operations) described therein without departing from the spirit of the invention. For instance, the steps may be performed in a differing order, or steps may be added, deleted or modified. All of these variations are considered a part of the claimed invention.

[0071] Although preferred embodiments have been depicted and described in detail herein, it will be apparent to those skilled in the relevant art that various modifications, additions, substitutions and the like can be made without departing from the spirit of the invention and these are therefore considered to be within the scope of the invention as defined in the following claims.